# Dupline ${ }^{\oplus}$ Field- and Installationbus Dupline ${ }^{\oplus}$ DeviceNet Gateway Type G 38910050 



- Built-in Dupline ${ }^{\otimes}$ channel generator
- DeviceNet slave
- DeviceNet communication speed of up to 500K Baud
- Read/control of 128 Dupline ${ }^{\oplus}$ inputs/outputs through DeviceNet
- Split-I/O mode selectable (128 inputs and 128 outputs)
- Support of $31 / 2$ digit BCD and AnaLink analog formats
- All Dupline ${ }^{\oplus}$ formats (Exept 8 bit binary multiplexed) avalible on the DeviceNet network
- For mounting on DIN-rail (EN 50 022)
- LED indicators for supply, Dupline ${ }^{\circledR}$ carrier and fault
- AC power supply


## Product Description

Dupline Channel Generator with the function of a DeviceNet slave. This means that the 128 Dupline ${ }^{\oplus}$ I/O's can be read/controlled by DeviceNet
masters (PLC's, PC interface cards, etc. from various suppliers). Several Dupline ${ }^{\oplus}$ gateways can be connected to the same DeviceNet network.

## Ordering Key

G 38910050230
Type: Dupline ${ }^{\circledR}$
H8-Housing
Type no.
Supply

## Type Selection

| Supply | Ordering no. |
| :--- | :--- |
| $115 / 230$ VAC | G 38910050 |

## Input/Output Specifications

| DeviceNet |  |
| :---: | :---: |
| Pin assignment V- | Pin 1 |
| CAN-L | Pin 2 |
| SHIELD | Pin 3 |
| CAN-H | Pin 4 |
| V+ | Pin 5 |
| Baudrate <br> Cable length (Thick cable) | Switch settings |
|  | 100 m @ 500K Baud |
|  | 200 m @ 250K Baud |
|  | 1200 m @ 125K Baud |
| Update time (128 digital I/O) | Typ. $200 \mu \mathrm{~s}$ at 560K Baud |
|  | Typ. 1.6 ms at 125K Baud |
| Dielectric voltage |  |
| DeviceNet Dupline ${ }^{\oplus}$ | $\geq 4 \mathrm{kVAC}$ (rms) |
| EDS-file |  |
| Dupline ${ }^{\text {® }}$ |  |
| Output voltage | 8.2 V |
| Output current | $\leq 100 \mathrm{~mA}$ |
| Short-circuit protection | Yes |
| All channels ON detector | Yes |
| Output impedance | $\leq 15 \Omega$ |
| Sequence time |  |
| 8 digital I/O | 15.2 ms |
| 128 digital I/O | 132.3 ms |
| AnaLink value update time |  |
| 8 signals128 signals | 3.9 s |
|  | 33.8 s |

## Adjustments

$1 \times 16$ pos. rotary switch
DIP-switch 1
DIP-switch 2
DIP-switch 3
DIP-switch 4

## Approvals

CE-marking

No. of Dupline ${ }^{\circledR}$ channels 8 .. 128 in steps of 8 Dupline ${ }^{\oplus}$ mode (Normal/Split I/O) Dupline ${ }^{\star}$ data transfer mode Analog input Analog output

## General Specifications

| Power ON delay | < 2.5 s until start of Dupline ${ }^{\circledR}$ carrier. $<40$ s until correct reading of AnaLink values |
| :---: | :---: |
| Indication for |  |
| Supply ON | LED, green |
| Dupline ${ }^{\text {® }}$ carrier | LED, yellow |
| Fault | LED, red |
| Environment |  |
| Degree of protection | IP 20 |
| Pollution degree | 3 (IEC 60664) |
| Operating temperature | $0^{\circ}$ to $+50^{\circ} \mathrm{C}\left(+32^{\circ}\right.$ to $\left.+122^{\circ} \mathrm{F}\right)$ |
| Storage temperature | $-20^{\circ}$ to $+85^{\circ} \mathrm{C}\left(-4^{\circ}\right.$ to $\left.+185^{\circ} \mathrm{F}\right)$ |
| Humidity (non-condensing) | 20 to 80\% RH |
| Mechanical resistance |  |
| Shock | $15 \mathrm{G}(11 \mathrm{~ms})$ |
| Vibration | 2 G (6 to 55 Hz ) |
| Dimensions Material | H8-housing (see Technical information) |
| Weight | 540 g |

## Supply Specifications



## Wiring Diagrams



## Mode of Operation

The Dupline ${ }^{\circledR}$ DeviceNet Gateway is a Dupline ${ }^{\circledR}$ Channel generator with a function of a DeviceNet slave. This means that the 128 Dupline ${ }^{\circledR}$ I/O's can be read/controlled by DeviceNet masters like PLC's and PC interfacecards from many different suppliers. Several Dupline ${ }^{\oplus}$ Gateways can be connected to the same network and operate together with other DeviceNet modules like operatorpanels, MMI's I/O modules etc.

## Configuration switches

The unit is equipped with the following configuration switches. (See also switch settings)
$1 \times 16$ position rotary-switch for selecting Number of Dupline ${ }^{\circledR}$ Channels in the range $8 . .128$ (in steps of 8).
The selected letter indicates the last channel group available on Dupline ${ }^{\oplus}$. If e.g. H is selected, the 64 channels in groups A..H will be available.

1x DIP-switch for selection of Dupline ${ }^{\circledR}$ Operation Mode. In "Normal" mode, Dupline ${ }^{\circledR}$ operates as a peer-to-peer system where the channel generator automatically establishes a connection between Dupline ${ }^{\circledR}$ inputs and Dupline ${ }^{\circledR}$ outputs which are coded to the same Dupline ${ }^{\oplus}$ address. If e.g. an input
coded for B5 is activated, the output(s) coded for B5 will also be activated. Consequently, a Dupline ${ }^{\text {® }}$ output can either be activated through the output-data received on DeviceNet or by an active Dupline ${ }^{\circledR}$ input coded for the same Dupline ${ }^{\circledR}$ address.
In "Split I/O" mode, the Dupline ${ }^{\star}$ inputs and Dupline ${ }^{\ominus}$ outputs are treated independently by the channel generator. If e.g. and input coded for B5 is activated, the Gateway will make the information available on DeviceNet (like in normal mode), but it will not automatically activate the Dupline ${ }^{\circledR}$ output(s) coded to B5. The Dupline ${ }^{\text {® }}$ outputs are controlled exclu-
sively through the output data received on DeviceNet.
$1 \times$ DIP-switch for selection of analog data.
In OFF position only Digital In/Out data are transferred.
To enable analog data-l/O handling this DIP-switch must be ON.
$1 \times$ DIP-switch for selection of Analog input operation mode. When OFF the analog input data are read as AnaLink. Each channel from C1 to P8 are read as 8 bit analog data. When ON the analog input data are considered as $31 / 2$ digit multiplexed data. The multiplex-control (Synchronization) are automatically set

## Mode of Operation (cont.)

to operate on channels A1..A4 which then can not be used for other purposes.

1xDIP-switch for selection of Analog Output operation mode.
When OFF the Analog output are emitted as AnaLink.
When ON the Analog Out-put-data are emitted as 3 1/2 digit Multiplexed data, and channels A1..A4 will control the multiplex addressing .

Dupline Input Data
A part of the Gateway inputprocessor reads all the 128

Dupline-channels as Digital inputs (16 bytes) and another part reads the 112 channels (C1 to P8) as Analog inputs and performs the appropriate scaling of input data. Each Analog value are represented as a 16 bit word with MBS as sign and 15 bits of magnitude. This results in a total of 224 bytes contaning all analog input-data.
All data are mapped with Digital input bytes starting at relative address 00 followed by the analog data. See In/out data mapping.

## Dupline ${ }^{\oplus}$ Output Data

Digital Output data are handled in accordance with the Dupline ${ }^{\oplus}$ Operation Mode: Split I/O or Normal. Analog data are handled and scaled in accordance to Analog Output operation mode.

When Analog Output is selected, care should be taken to avoid a mix of Digital and analog output data.
The AnaLink Outputs a series of pulsating 1's and 0's and for the value of zero, a basic 8 pulse-train will be outputed, for enabling the
receivers to detect validity. When outputting Multiplexed Analog, two bits are output for format-check.

To disable analog outputs, write a value of -32767 (0xFFFF) in all locations where only digital data should be. If eg. channels O1 .. P8 are desired as purly digital data, all analog data bytes mapped from relative address OxDO to OXEF should be written the value $0 x F F$.

## Relative addressing af Input/output data

| Input/Output area |
| :--- |
| adr:    <br> 00: A-P Digital 16 bytes <br> 10: C-D Analog 32 bytes <br> $30:$ E-F Analog 32 bytes <br> $50:$ G-H Analog 32 bytes <br> $70:$ I-J Analog 32 bytes <br> $90:$ K-L Analog 32 bytes <br> B0: M-N Analog 32 bytes <br> D0: O-P Analog 32 bytes |

## Digital data

| Adr: | MSB | LSB |
| :--- | :--- | :--- |
| 00: | A1. | A8 |
| 01: | B1. |  |
| 02: |  |  |

## Analog data

| Adr: |  |
| :--- | :--- |
| 10-11: | C1: AnaLink or C-D mux 0 |
| 12-13: | C2: AnaLink or C-D mux 1 |
|  |  |
| $2 \mathrm{EE}-2 \mathrm{~F}:$ | D8: AnaLink or C-D mux 15 |



## Pin Assignment - DeviceNet Connector



BUS connector

| Plugable connector | Screw terminals | Description |
| :---: | :---: | :---: |
| 1 | 1 | V- |
| 2 | 2 | CAN-L |
| 3 | 3 | SHIELD |
| 4 | 4 | CAN-H |
| 5 | 5 | V+ |

## Network Status Module Status

Module errors are indicated with the Module status LED and Network status LED

| LED's | Description |
| :---: | :---: |
| Module-Status, steady off | No power |
| Module-Status, steady red | Unrecoverable fault |
| Module-Status, steady green | Device Operational |
| Module-Status, flashing red | Minor fault |
| Network-Status, steady off | Not Powered/Not on line |
| Network-Status, steady green | Link OK on line, Connected |
| Network-Status, steady red | Critical Link failure |
| Network-Status, flashing green | On line not connected |
| Network-Status, flashing red | Connection Time Out |

## Switch Settings



